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JC971 U.S. PTO  
09/897902  
07/05/01

XA-9512  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Isamu HAYASHI et al.

Appln. No.:

Filed: Herewith

For: MICROPROCESSOR, SEMICONDUCTOR MODULE AND DATA  
PROCESSING SYSTEM

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion  
as to materiality or prior art effect, the document listed  
on the attached Form PTO-1449 is hereby cited.

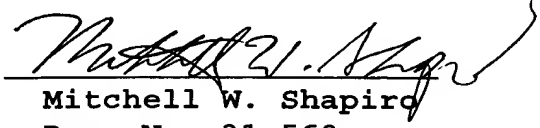
The document on the attached List is cited in the  
specification, on page 3, and its relevance is addressed  
therein.

Respectfully submitted,

MWS:SJK

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By:

  
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Reg. No. 31,568

July 5, 2001